

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph 0015 as follows:

Various embodiments of the invention relate to communicating data between a number of processing elements of a signal processor, using a plurality of communication registers mapped into the address space of each processing element. Specifically, shared communication registers mapped into the address space of multiple processing elements may more quickly and more efficiently communicate data, such as graphics data, between the multiple processing elements. For instance, each of the communication registers may couple a first of the processing elements to every other processing element. Thus, faster and more efficient communication of data between the processing elements is provided as compared to sharing data between processing elements using separate registers between each pair of processing elements, and/or using registers having addressing that are not mapped into the addressing space of each processing element. For example, **Figure 1A** is a block diagram of a cluster of nine interconnected image signal processors (ISP) coupled to double data rate (DDR) random access memory (RAM) (e.g., such as DDR Synchronous Dynamic (SD) RAM) and having interfaces for coupling to other ISP clusters. **Figure 1A** shows signal processing system 100 having a cluster of nine digital signal processors, also referred to as “image signal processors” (ISP), where each signal processor is coupled to at least one other signal processor and may also be coupled to one or more various types of memories and/or other signal processing clusters (e.g., such as other signal processing systems similar to system 100). For example, a hierarchical image processing image architecture similar to system 100 can be used for image processing related to a copier, a scanner, a printer, or other image processing device including to process a raster image, a Joint Photographic Experts Group (JPEG) image, a Moving Picture Experts Group (MPEG) image, or other digital image data.